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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

NONE

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-269216 09/05/2001

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 44	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i>	INITIALS <i>[Initials]</i>		

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## TITLE

Test circuit and semiconductor integrated circuit effectively carrying out verification of connection of nodes

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